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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eric Reitingner
Serial No: Unassigned
Filed: Currently herewith
Int'l. Patent Appln. No.: PCT/EP03/03937
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For: METHOD AND DEVICE FOR CONDITIONING SEMICONDUCTOR
WAFERS AND/OR HYBRIDS

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

IN THE CLAIMS:

1. (Currently Amended) Method for conditioning semiconductor wafers and/or hybrids having the steps:

preparation of a space (1) which is at least partially enclosed and has a wafer/hybrid holding device (10) which is located therein and has the purpose of holding a semiconductor wafer and/or hybrid; and

CERTIFICATE OF MAILING 37 C.F.R. §1.10.

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) are being deposited with the United States Postal Service on this date October 15, 2004 in an envelope as "Express Mail Post Office to Addressee" Mailing Label Number EV 511477554US addressed to: Mail Stop PCT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: 10/15/04


Frank Chau